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				Application No	09/843,486
				Filing Date	April 25, 2001
				First Named Inventor	Hassibi, et al
				Group Art Unit	Not Yet Assigned
				Examiner Name	Not Yet Assigned
Sheet	1	of	1	Attorney Docket Number	004363.P004

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

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OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

Examiner Initials*		Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), publisher, city and/or country where published (if title of the item known). (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s).	Translation? Yes/No
QVU		HERSHENSON, MARIA DEL MAR, et al, "Optimization of Inductor Circuits via Geometric Programming," Electrical Engineering Department, Stanford University	
QVU		MOHAN, SUNDERARAJAN S. et al, "Bandwidth Extension in CMOS with Optimized On-Chip Inductors," IEEE Journal of Solid-State Circuits, Vol. 35, No. 3, March 2000, pp.0346-355	
QVU		KIM, CHUNG-HWAN, et al, "Frequency-Dependent Series Resistance of Monolithic Spiral Inductors," IEEE Microwave and Guided Wave Letters, Vol. 9, No. 12, 12/1999 pp. 514-516	

Examiner	<i>Curran</i>	Date Considered	<i>07/22/02</i>
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